

A M E N D M E N T

IN THE CLAIMS:

Please cancel claims 15-16, 18-19, and 27 without prejudice so that the claims read as follows:

1. (Previously Presented) A method for controlling the flow of data between a first and second clock domain comprising:

selecting one of a plurality of ports included in a physical layer interface in the second clock domain to which to send data; and

transmitting data from a transmit buffer in the first clock domain to the selected port in the physical layer interface in the second clock domain,

wherein selecting one of the plurality of ports included in the physical layer interface in the second clock domain to which to send data includes:

polling each of the plurality of ports in the physical layer interface in the second clock domain to determine polling results indicating available ports which may receive data;

sending the polling results to the first clock domain; and

selecting, in the first clock domain, a port from the available ports included in the physical layer interface in the second clock domain to which to send data, and

wherein sending the polling results to the first clock domain includes sending the polling results to the first clock domain after a predetermined portion of a previous data transmission from the transmit buffer in the first clock domain

to the selected port in the physical layer interface in the second clock domain is complete.

2. (Canceled)

3. (Original) The method of claim 1 wherein transmitting data from the transmit buffer in the first clock domain to the selected port in the physical layer interface in the second clock domain includes:

transmitting data for the selected port from the transmit buffer to an asynchronous buffer;

transmitting the data from the asynchronous buffer to an internal buffer of a physical layer device included in the physical layer interface, the physical layer device including the selected port; and

transmitting the data from the internal buffer of the physical layer device to the selected port.

4. (Previously Presented) The method of claim 3 further comprising resetting the polling results after a predetermined portion of a data transmission from the asynchronous buffer to the selected port in the physical layer interface in the second clock domain is complete.

5. to 27. (Canceled)